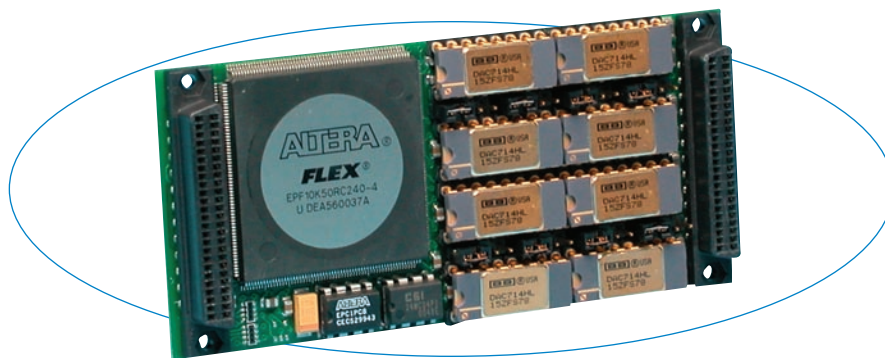


IP236A-8 16-bit D/A Analog Output with FIFO Buffers



Industry Pack Modules

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IP236A-8 modules have 16-bit D/A converters (DAC) to provide highly-accurate analog voltage outputs. FIFO buffers enhance control over the transfer of data to the DAC and improve efficiency.

Each channel has a dedicated 128 sample FIFO buffer and its own clock. A start trigger transfers digital values from the buffer to the DAC. Three modes offer several choices for the data transfer on each channel. Continuous mode cycles data through the buffer for a given channel and is ideal for waveform generation. As new data loads into the FIFO, the output signal instantly updates without stopping the waveform. Single mode moves one value from the buffer to the converter for each trigger. External trigger mode synchronizes channel conversions to an event or other IP236A-8 modules.

Features

- 8 analog voltage outputs
- Individual 16-bit D/A converter on each channel
- Individual clock on each channel supporting rates of up to 100KHz
- FIFO memory buffers (128 samples/channel)
- Software, external, or internal timer triggers
- Interrupt capability
- External trigger output
- Extended temperature option (-40 to 85°C)

Benefits

- FIFO buffers enable “on-the-fly” changes to the output waveform as new data is received.
- Internally-stored calibration coefficients ensure accuracy.
- Independent control of each channel enables individual updates and unique conversion rates.

The IP236A-8A is ideal for generating waveforms. Large FIFO buffers reduce CPU interactions for increased overall performance.

Specifications

Analog Outputs

- Output configuration: 8.
- D/A Resolution: 16 bits.
- Output ranges: $\pm 5V$, $\pm 10V$, 0 to 10V (jumper-selectable).
- Data sample memory: 128 sample FIFO buffer on each channel.
- Maximum throughput rate:
 - Outputs can be updated simultaneously or individually.
 - One channel: 100KHz (10 μ S/conversion)
 - Eight channels: 100KHz (10 μ S/8 ch).
- DAC programming: Independent. Input registers and FIFOs are directly loaded.
- System accuracy: 0.0061% of 20V span maximum corrected error (i.e. calibrated) at 25°C with the output unloaded.
- Output at reset: 0V for bipolar output, 5V for unipolar.
- Output current: -5 to +5mA (maximum).
- Short circuit protection: Indefinite at 25°C.

IP Compliance (ANSI/VITA 4)

- Meets IP specifications per ANSI/VITA 4-1995.
- IP data transfer cycle types supported:
 - Input/output (IOsel*), ID read (IDsel*), Interrupt select (INTsel*).
- Access Times (8MHz clock):
 - All functions: 0 wait states (250nS cycle) except
 - FIFO buffer write: 2 wait state (500nS cycle),
 - Interrupt read/write: 2 wait states (250nS cycle).

Environmental

- Operating temperature: 0 to 70°C (IP236A-8) or -40 to 85°C (IP236-8E models).
- Storage temperature: -55 to 125°C (all models).
- Relative humidity: 5 to 95% non-condensing
- Power:
 - +5V ($\pm 5\%$): 250mA maximum..
 - $\pm 12V$ ($\pm 5\%$) from P1: 210mA maximum.
- MTBF: Consult factory.

Ordering Information

Industry Pack Modules

IP236A-8

Eight voltage output channels.

Acromag offers a wide selection of [Industry Pack Carrier Cards](#).

Software

IPSW-API-VXW

VxWorks® software support package

IPSW-API-WIN32

32-bit Windows® DLL driver software support package

IPSW-API-WIN64

64-bit Windows® DLL driver software support package

IPSW-API-LNX

Linux® support (website download only)

Accessories

See www.acromag.com for more information

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